

**In the Claims**

1. (currently amended) A voltage converter device for converting a signal at an initial voltage level into a signal at a second voltage level which is different to the initial voltage level, the voltage converter device comprising an amplifier device,

wherein for generating signals at the second voltage level, a first and a second output signal of the amplifier device are used, and wherein the second output signal of the amplifier device differs from the first output signal of the amplifier device,

the voltage converter additionally comprising a first and second transmission gate;

the first transmission gate being driven by the first output signal of the amplifier device, and the second transmission gate being driven by the second output signal of the amplifier device;

an output of the first transmission gate being connected to an output of the second transmission gate to form the signal at the second voltage level.

2. (previously presented) A voltage converter device according to Claim 1, in which the first and the second output signals are mutually complementary signals.

3. (previously presented) A voltage converter device according to Claim 1, in which a flank of the first output signal triggers the signal at the second voltage level to change from a first state to a second state, and in which a flank displaced in time in relation to the flank of the first output signal, triggers the signal at the second voltage level to change from the second state back into the first state.

4. (previously presented) A voltage converter device according to Claim 3, in which the triggering flank of the first output signal is a positive flank, and the triggering flank of the second output signal is also a positive flank.

5. (previously presented) A voltage converter device according to Claim 3, in which the triggering flank of the first output signal is a negative flank, and the triggering flank of the second output signal is also a negative flank.

6. (cancelled)

7. (currently amended) A voltage converter device according to Claim 1 6, in which the first output signal of the amplifier device ~~output signal~~ or a signal derived from it, is used to switch through an input of the first transmission gate, ~~where a relatively high voltage is present,~~ to an output of the first transmission gate.

8. (currently amended) A voltage converter device according to Claim 3, in which the second output signal, or a signal derived from it, is used to switch through an input of the second transmission gate, ~~where a relatively low voltage — in particular a ground — is present,~~ to an output of the second transmission gate.

9. (previously presented) A voltage converter device according to Claim 8, in which the outputs of the transmission gates are connected to each other.

10. (previously presented) A voltage converter device according to Claim 9, in which the first voltage level is lower than the second voltage level.

11. (previously presented) A voltage converter device according to Claim 10, in which the first voltage level varies from 1.2 V to 1.9 V, but more particularly from 1.4 V to 1.6 V, and the second voltage level from 1.5 V to 2.2 V, but more particularly from 1.7 V to 1.9 V.

12. (previously presented) A voltage converter device according to Claim 11, in which the amplifier device has several cross-connected transistors.

13. (previously presented) A voltage converter device according to Claim 12, in which the transistors are field effect transistors.

14. (previously presented) A voltage converter device that converts a first signal having an initial voltage level to a second signal having a second voltage level that is different from the initial voltage level, comprising:

- an amplifier device for generating a first output signal and a second output signal, the second output signal being different from the first output signal;

- a first transmission gate driven by the first output signal and having an output;

- a second transmission gate driven by the second output signal and having an output;

- the first transmission gate output being connected to the second transmission gate output to form the second signal.